

Application No. 10/734,420
Amendment dated July 21, 2006
Office Action mail date: April 27, 2006

Atty. Docket No. MP0263 (13298/3)
PATENT APPLICATION

AMENDMENTS TO THE SPECIFICATION

Please amend paragraph 35 as follows:

[35] If a failed memory location is detected at step 203, the ERROR flag comes up at step 204, as shown in Fig. 3, and memory testing is frozen at the failed memory location. Otherwise, the process goes to step [[209]] 210, which will be described in more detail below.

Please amend paragraph 55 as follows:

[55] For each valid entry, an associated [[a]] flag RGTX will be set to denote more than X encounters of the stored row address, and a flag CGTY will be set to denote more than Y encounters of the stored column address. X is the number of available redundant columns and Y is the number of available redundant rows, wherein $X \geq 1$, and $Y \geq 1$. When an entry's flag RGTX is set, the failed memory locations with the same row address can only be repaired by a row, instead of by columns. When the flag CGTY is set, the failed memory locations with the same column address can only be repaired by a column, instead of by rows.